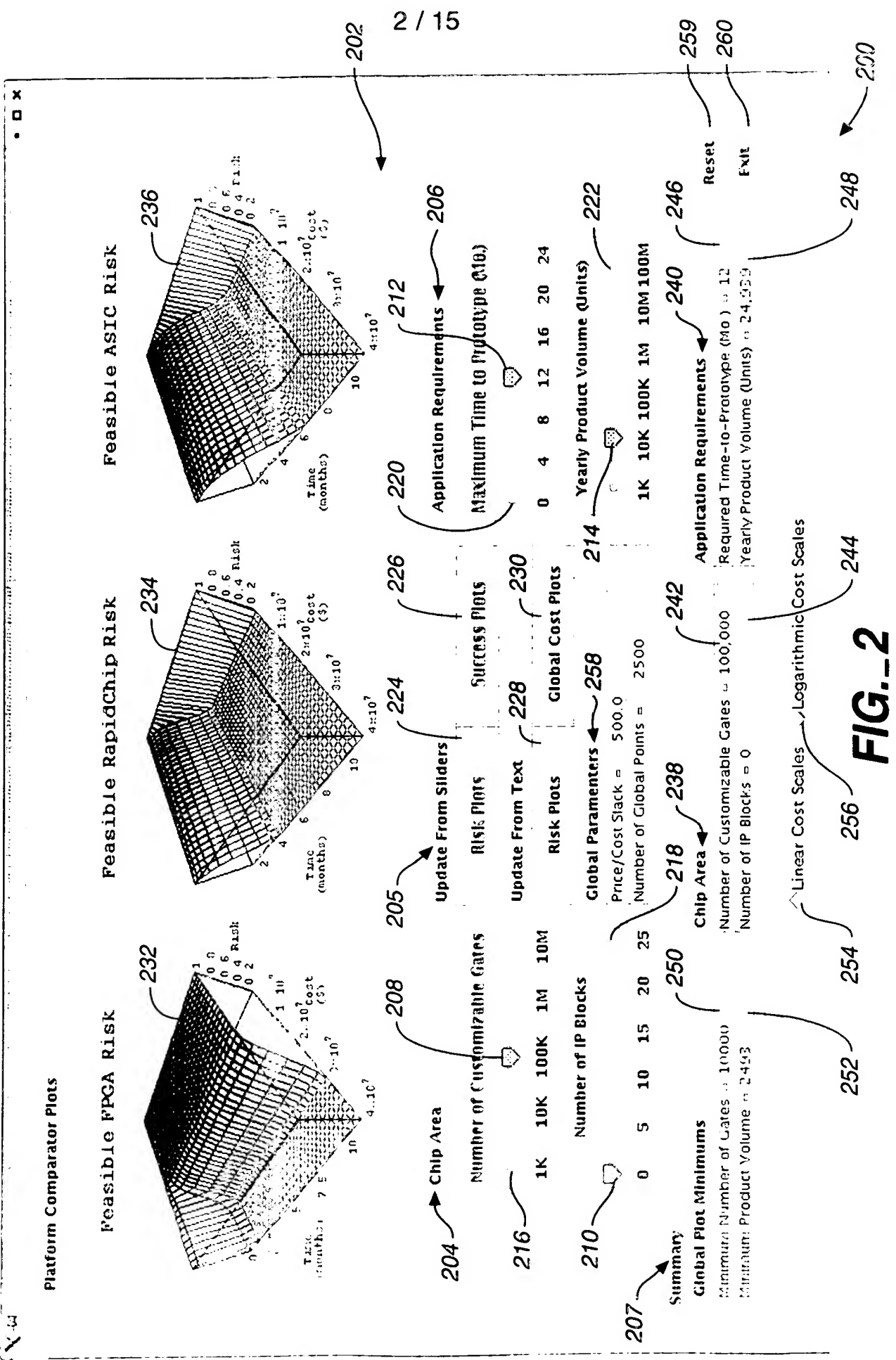


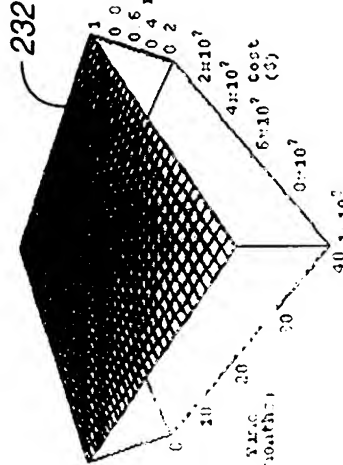
FIG._1



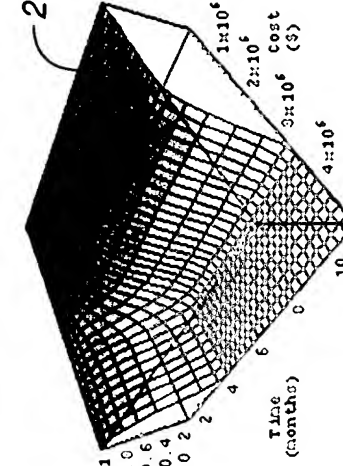
□ x

Platform Comparator Plots

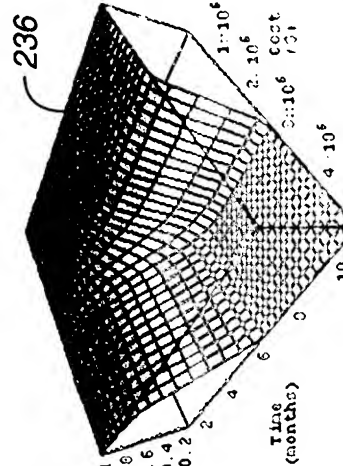
100K Infeasible at Given Set Point



Feasible RapidChip Risk



Feasible ASIC Risk



204 → Chip Area

Update From Sliders

Number of Customizable Gates

1K	10K	100K	1M	10M
0	5	10	15	20
25				

Risk Plots

Update From Text

Risk Plots

Global Parameters

Price/Cost Slack	500.0
Number of Global Points	2500

Success Plots

Global Cost Plots

206 Application Requirements

Maximum Time to Prototype (Mo.)

0	4	8	12	16	20	24
---	---	---	----	----	----	----

214 Yearly Product Volume (Units)

1K	10K	100K	1M	10M	100M
----	-----	------	----	-----	------

Summary

Global Plot Minimums

Minimum Number of Gates	10000
Minimum Product Volume	29980

Chip Area

Number of Customizable Gates	100,000
Number of IP Blocks	0

240 Application Requirements

Required Time-to-Prototype (Mo.)	12
Yearly Product Volume (Units)	299,800

Reset

Exit

Linear Cost Scales Logarithmic Cost Scales

252

248

FIG._3

200

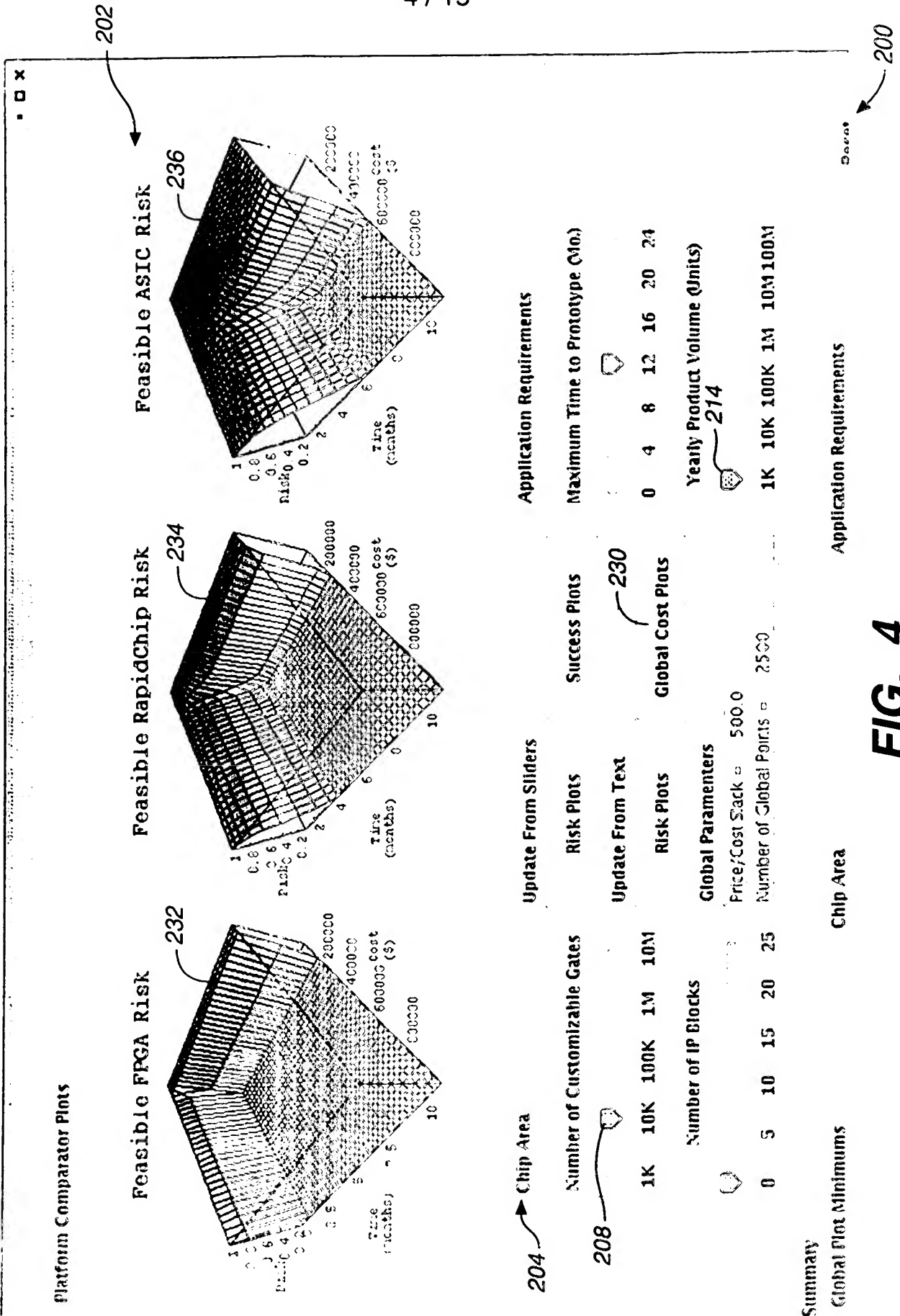
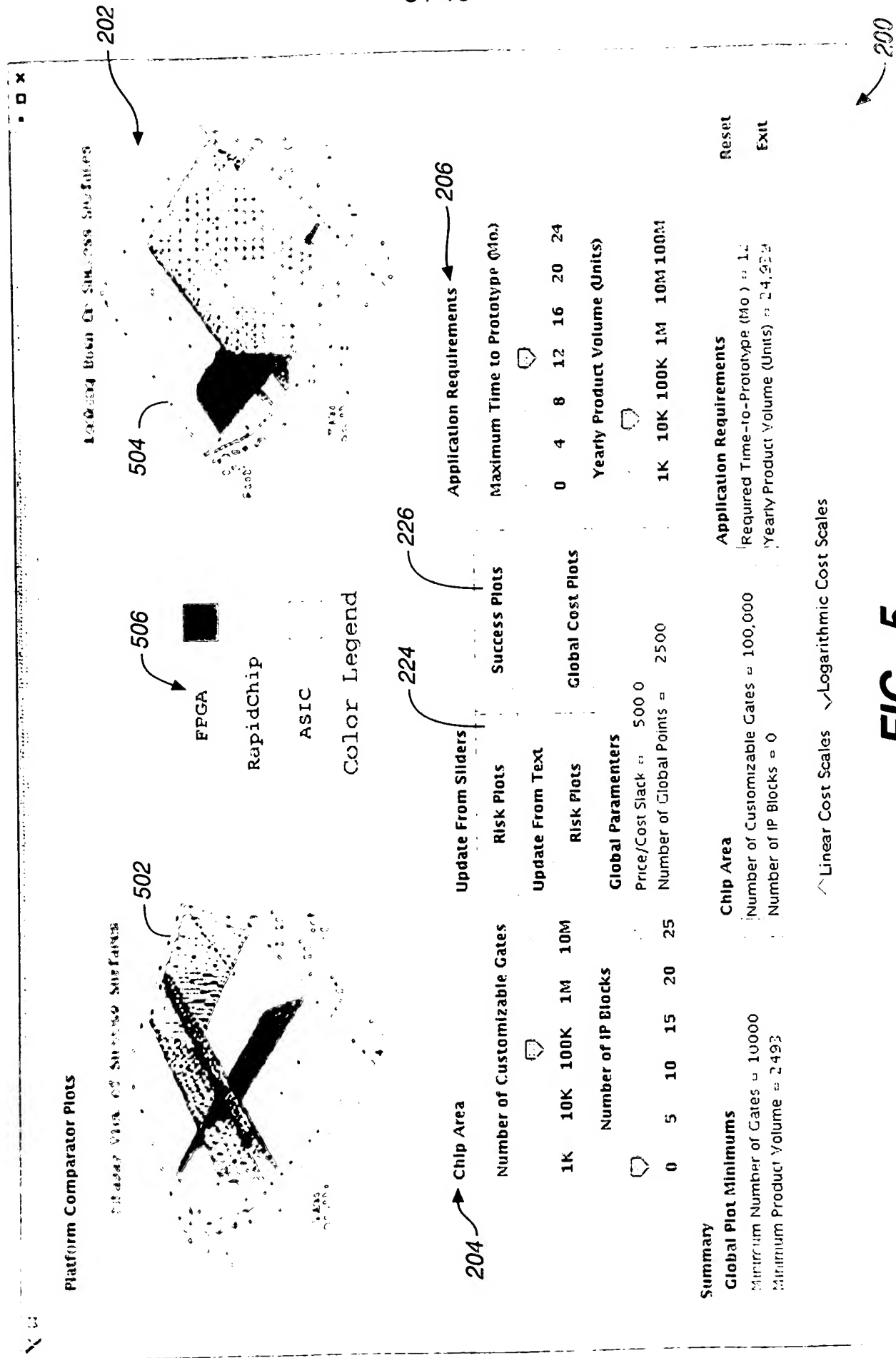
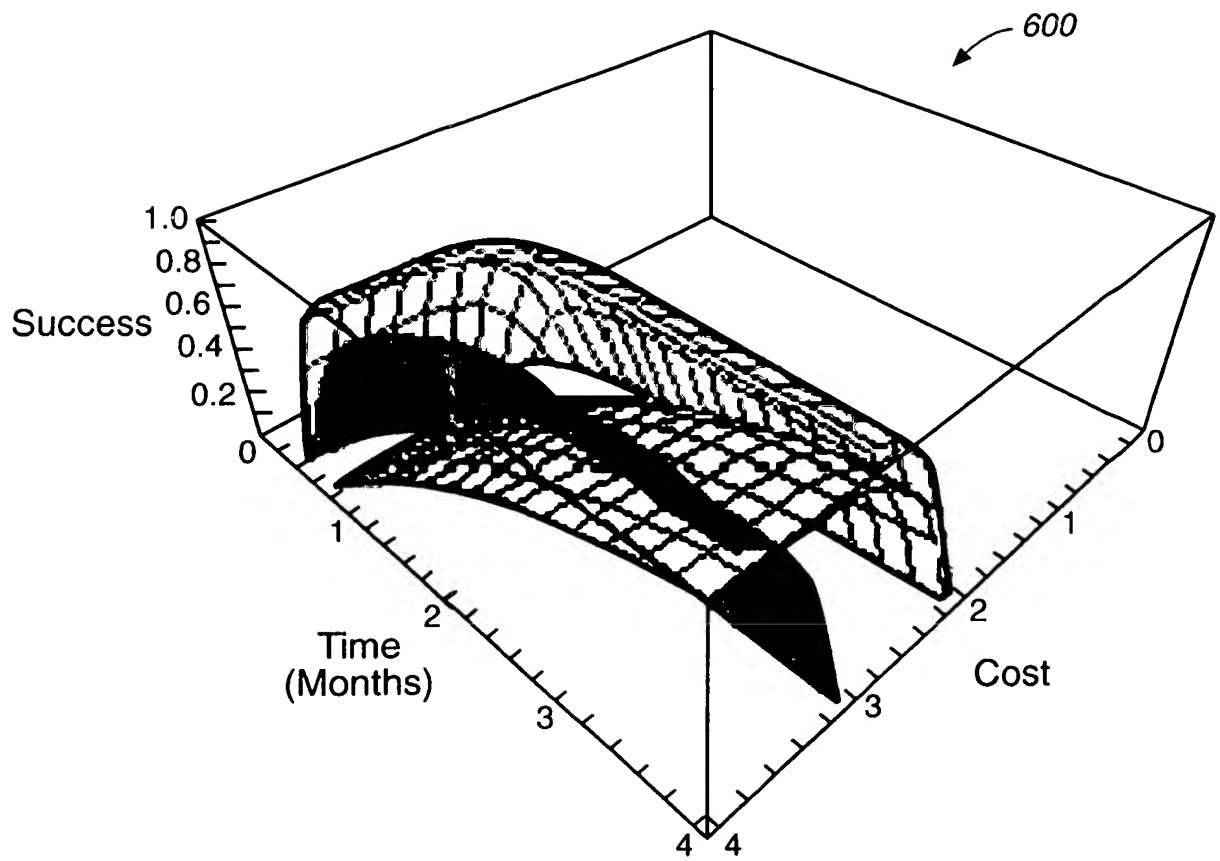
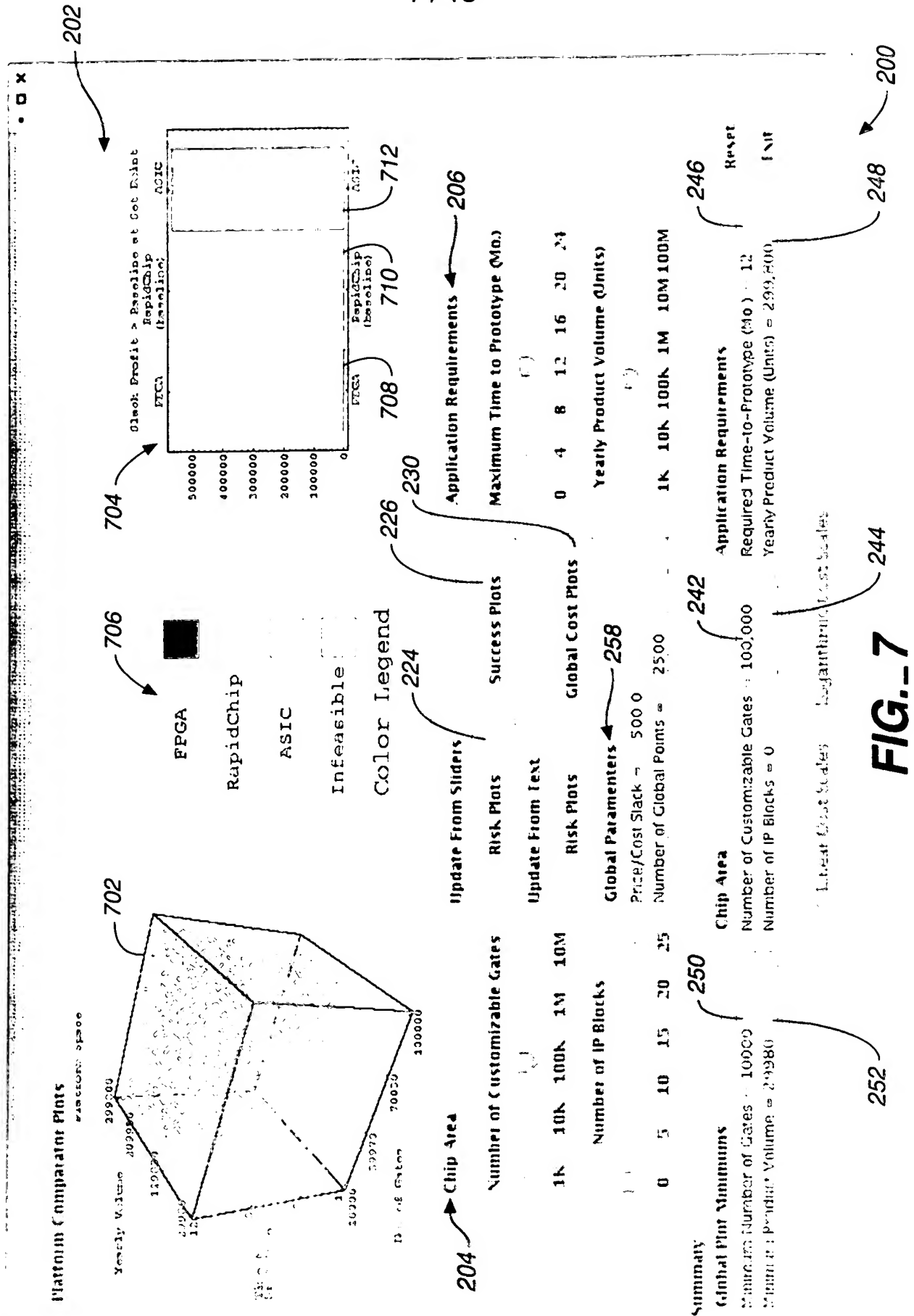


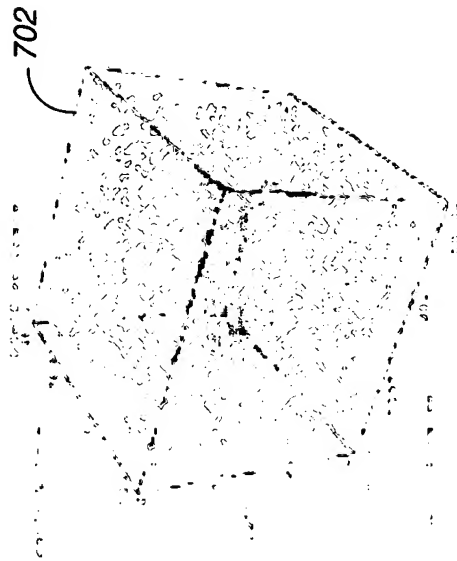
FIG. 4



**FIG. 6**



Platform Comparison Plots



204 → Chip Area

Number of Customizable Gates

1K 10K 100K 1M 10M

Number of IP Blocks

0 5 10 15 20 25

Summary

Global Plot Minimums

Minimum Number of Gates = 1000

Minimum Product Volume = 100

Chip Area

Number of Customizable Gates = 10,000

Number of IP Blocks = 0

Update From Sliders

Risk Plots

Update From Text

Risk Plots

Global Parameters

Price/Cost Slack = 500.0

Number of Global Points = 2500

Success Plots

Global Cost Plots

Application Requirements

Maximum Time to Prototype (Mo)

0 4 8 12 16 20 24

Yearly Product Volume (Units)

1K 10K 100K 1M 10M 100M

Application Requirements

Required Time-to-Prototype (Mo) = 12

Yearly Product Volume (Units) = 1,000

Reset

Exit

706

FPGA

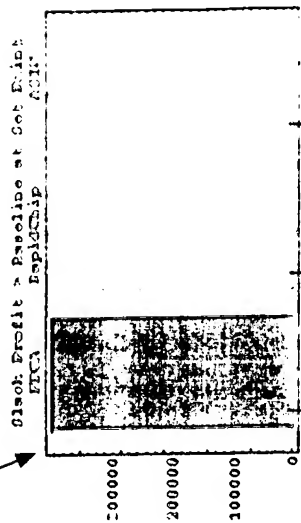
RapidChip

ASIC

Infeasible

Color Legend

704



708

710

712

230

Application Requirements

Maximum Time to Prototype (Mo)

0 4 8 12 16 20 24

Yearly Product Volume (Units)

1K 10K 100K 1M 10M 100M

Application Requirements

Required Time-to-Prototype (Mo) = 12

Yearly Product Volume (Units) = 1,000

Reset

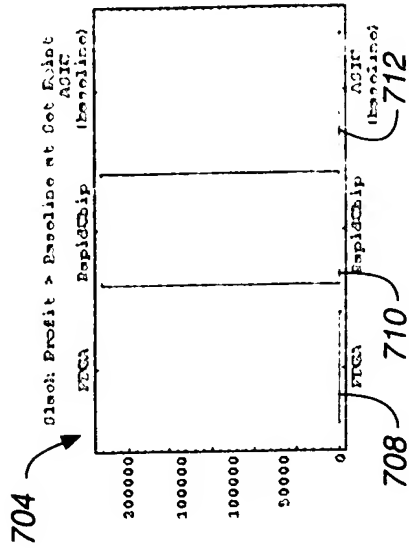
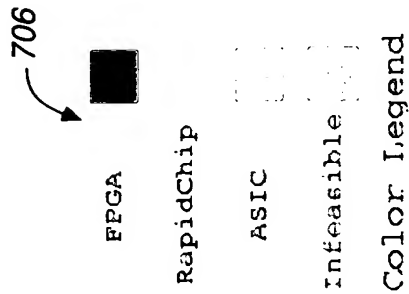
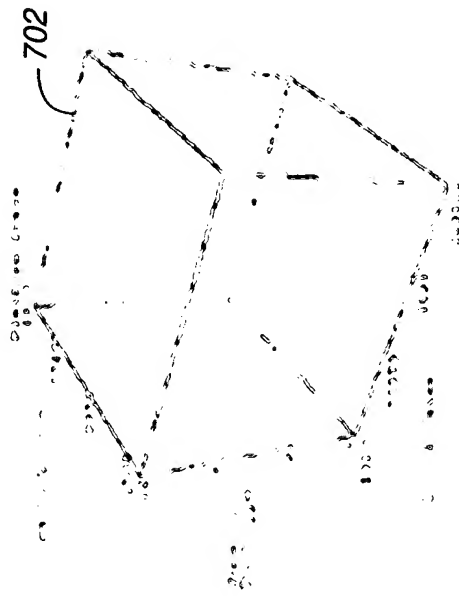
Exit

Linear Cost Scales Logarithmic Cost Scales

FIG._8

200

Platform Comparators Plots



Chip Area

Number of Customizable Gates

1K 10K 100K 1M 10M

Number of IP Blocks

0 5 10 15 20 25

Summary

Global Plot Minimums

Minimum Number of Gates = 10000

Minimum Product Volume = 2500

Chip Area

Number of Customizable Gates = 100,000

Number of IP Blocks = 0

Update From Sliders

Risk Plots

Update From Text

Risk Plots

Global Parameters

Price/Cost Slack = 500.0

Number of Global Points = 2500

Success Plots

Maximum Time to Prototype (Mo.)

0 4 8 12 16 20 24

Yearly Product Volume (Units)

1K 10K 100K 1M 10M 100M

Application Requirements

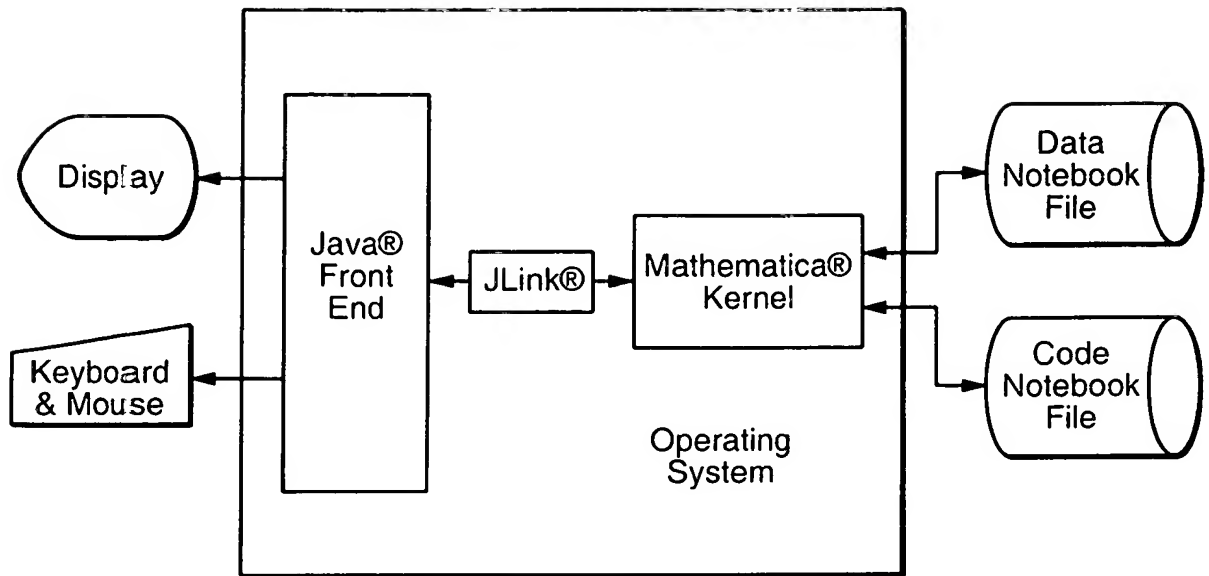
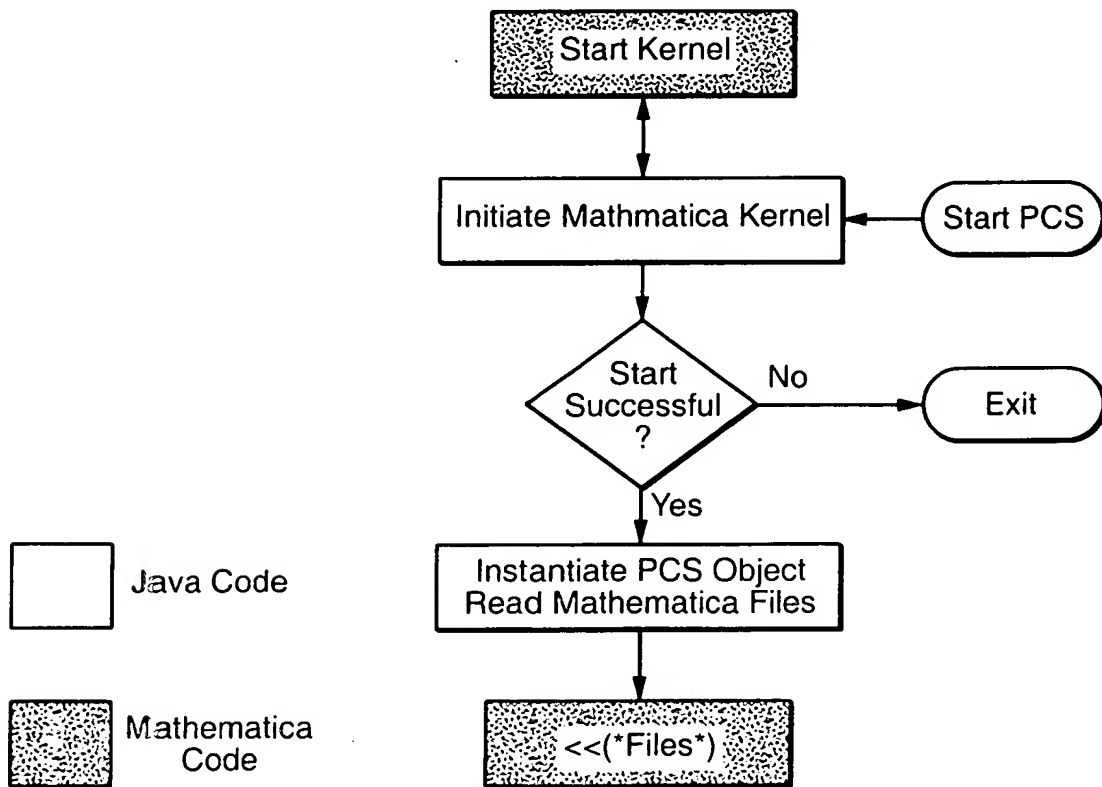
Required Time-to-Prototype (Mo.) = 12

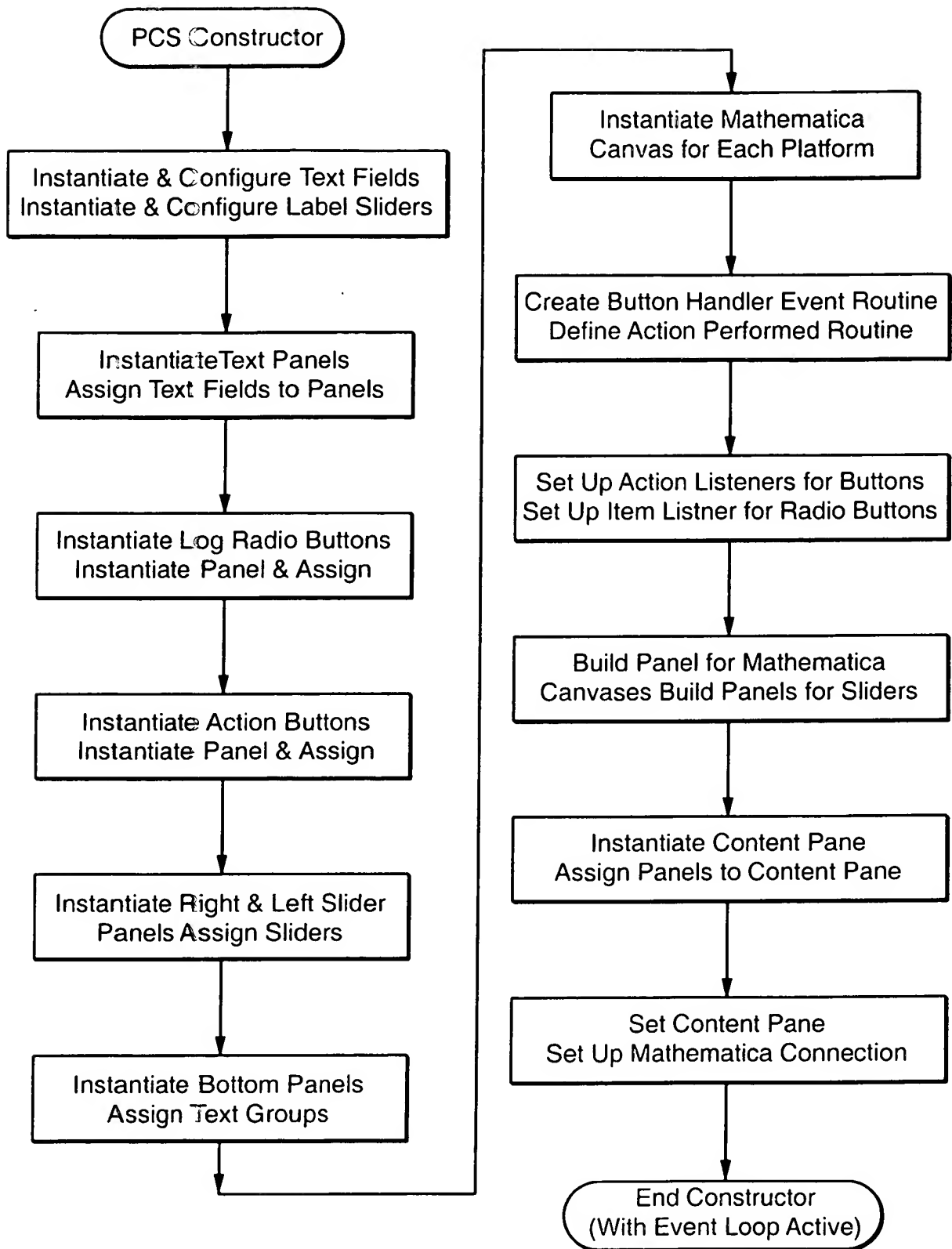
Yearly Product Volume (Units) = 25000

Reset

Exit

FIG.-9

**FIG._ 10****FIG._ 11**

**FIG._12**

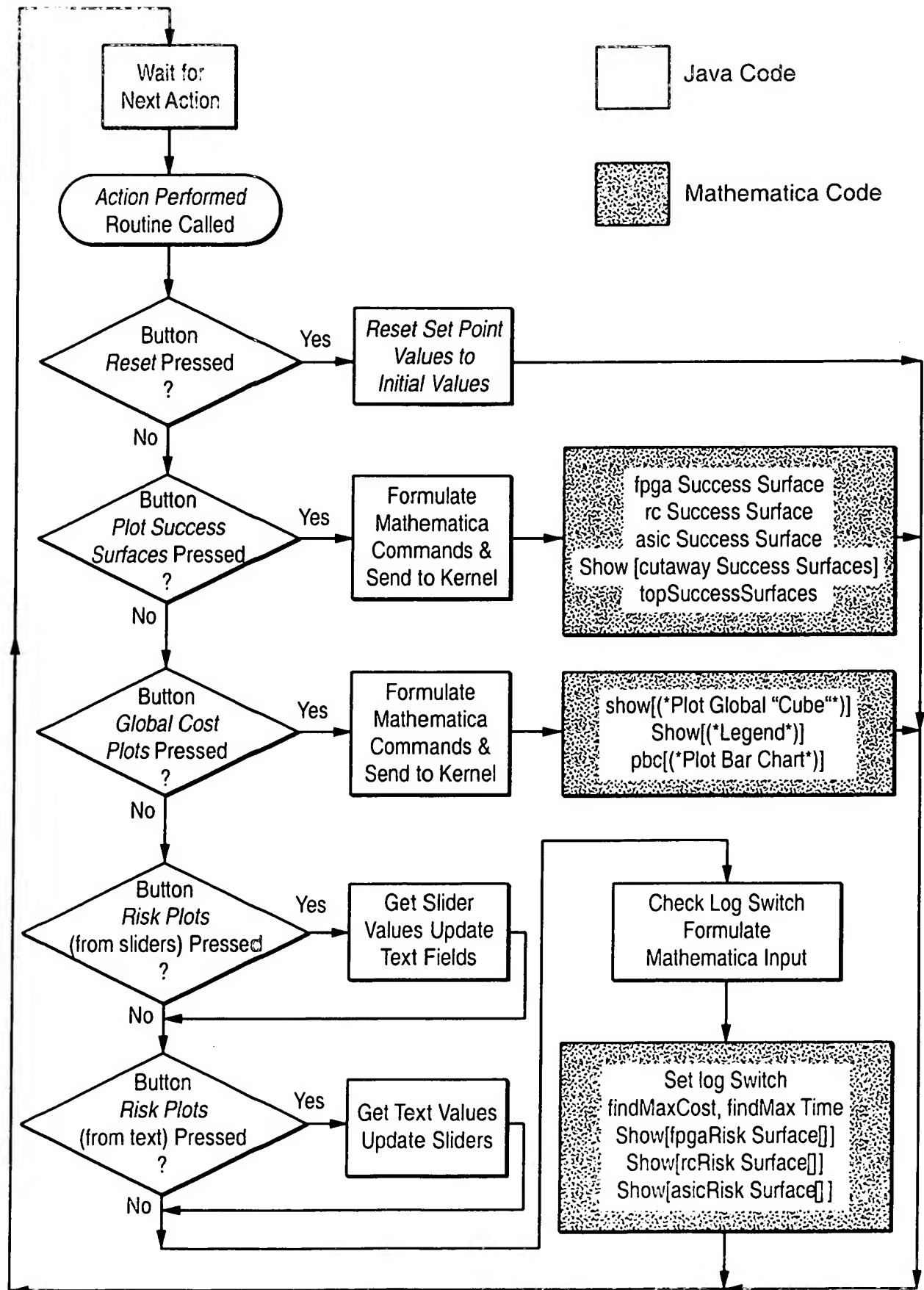
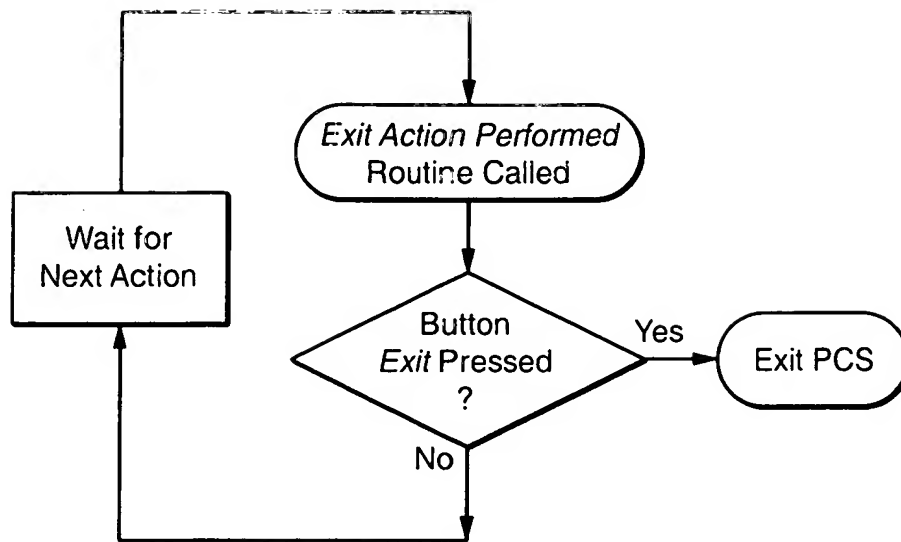
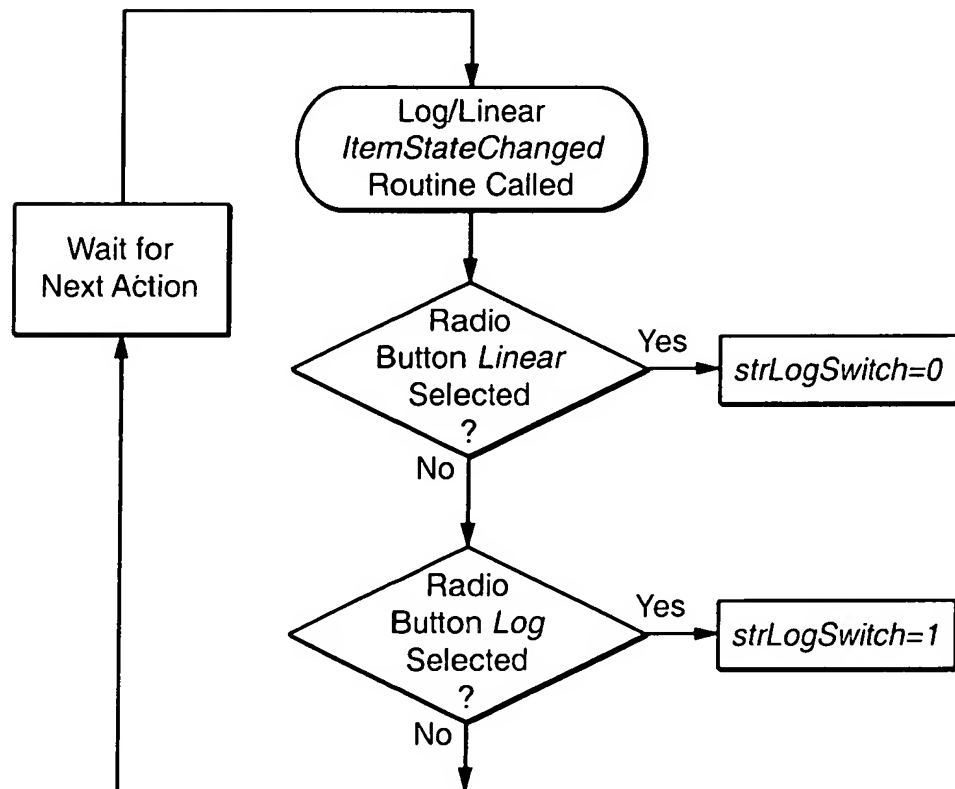
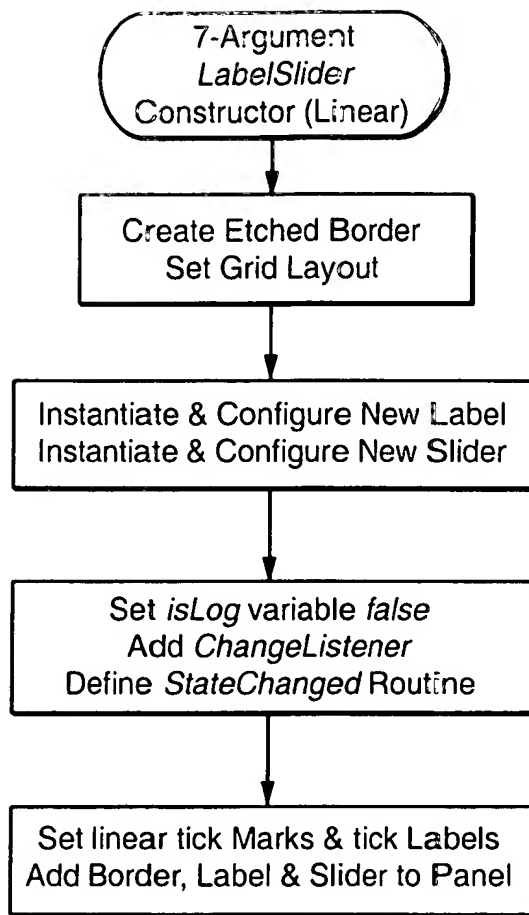
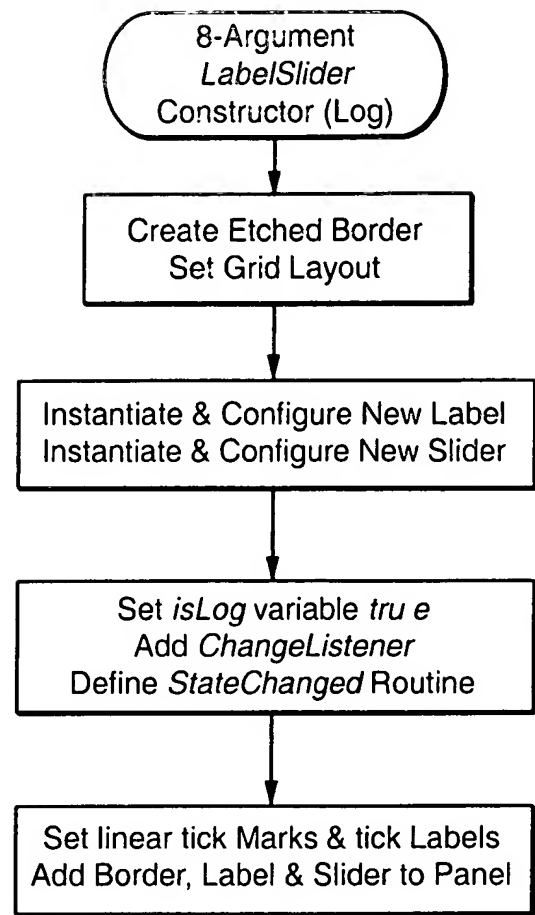
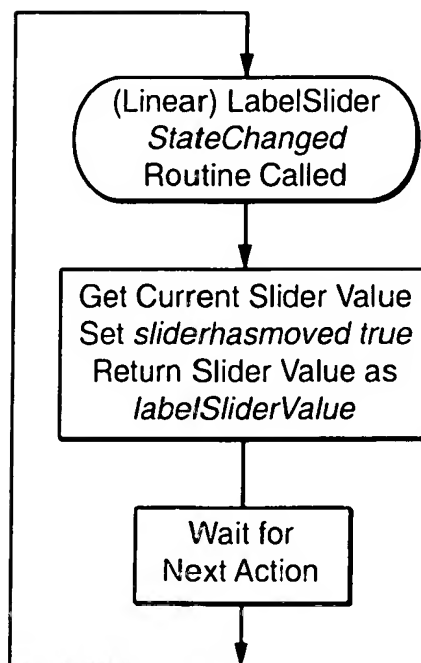
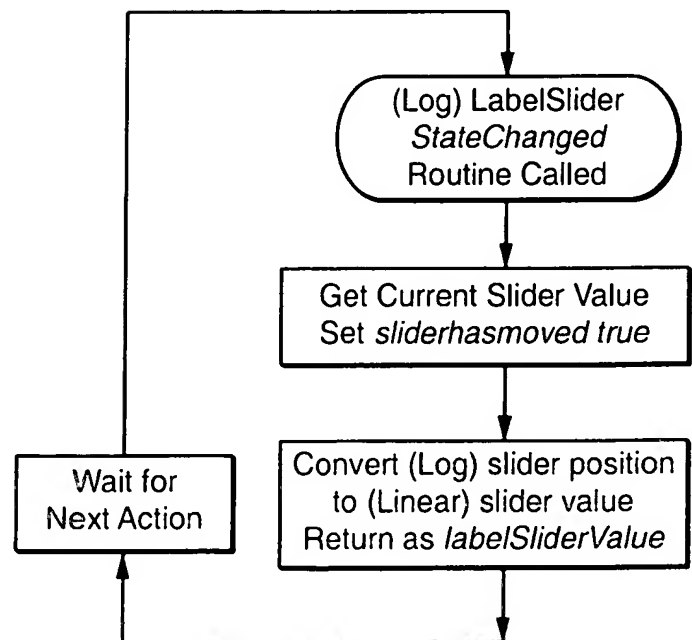
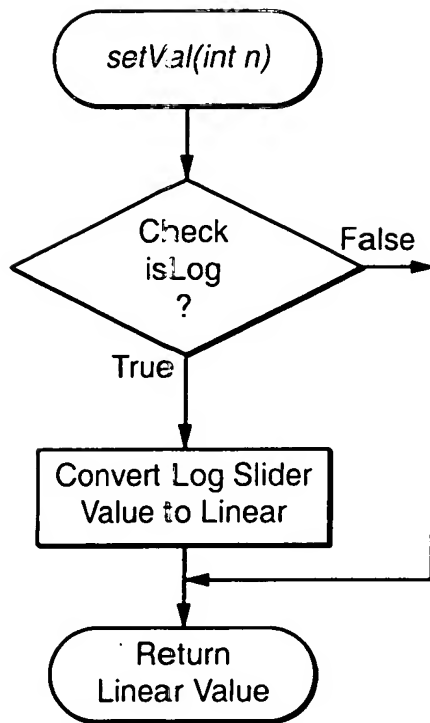
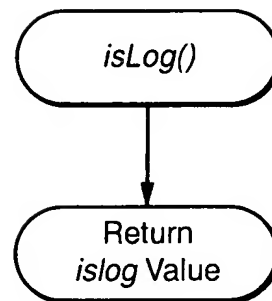
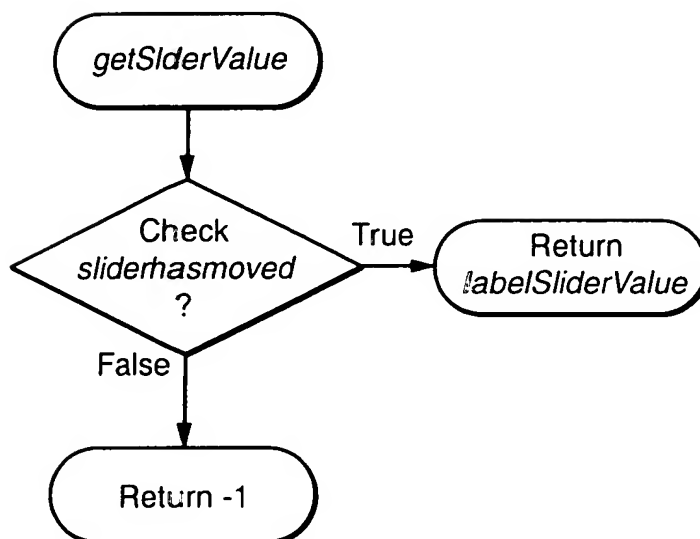


FIG. 13

**FIG. 14****FIG. 15**

**FIG._16****FIG._18****FIG._17****FIG._19**

**FIG._20****FIG._22****FIG._21**

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